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In re Application of:  
**AUSTEN ET AL.**

Examiner: **C. McCARThY**

**Serial No.: 10/087,920**

Art Unit: **2113**

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# For: METHOD AND SYSTEM TO IDENTIFY A MEMORY CORRUPTION SOURCE WITHIN A MULTIPROCESSOR SYSTEM

## LETTER TO THE OFFICIAL DRAFTSMAN

**DRAWING REVIEW BRANCH**  
U. S. Patent and Trademark Office  
Alexandria, Virginia 22313-1450

Sir:

Submitted herewith are five (5) sheets of formal drawings replacing the informal drawings filed initially with the above-referenced allowed application.

Respectfully submitted,

10/26/04  
(Date)

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